

Model Name: GA-B85M-DS3H

Revision 1.1

SHEET

TITLE

01	COVER SHEET
02	BOM & PCB MODIFY HISTORY
03	BLOCK DIAGRAM
04	CPU LGA1150-A
05	CPU LGA1150-B
06	CPU LGA1150-C
07	DDR III CHANNEL A 1,2
08	DDR III CHANNEL B 1,2
09	PCH FDI,DMI,USB,PCIE,NVRAM
10	PCH DP,CLK BUFFER
11	PCH HOST,SATA,PCI
12	PCH GPIO,CTRL,AUDIO
13	PCH PWR,GND
14	PCI EXPRESS*16 SLOT
15	PCI EXPRESS X1 *2 SLOT
16	ITE 8620 LPC IO
17	COM,KB MS USB,USB30 20
18	HWM,FAN CTRL,OV,-PROCHOT
19	DUAL BIOS
20	FP,FUSB,SPK,SATALED
21	Realtek ALC887-VD2
22	REAR AUDIO JACK
23	REALTEK RTL8111F
24	DISCRETE POWER
25	ATX
26	VCORE ISL95820 1
27	VCORE ISL95820 2

SHEET

TITLE

28	RT8120 DDR POWER
29	LPT, M3 POWER
30	DVI

Gigabyte Technology

Cover Sheet		
Size Custom	Document Number GA-B85M-DS3H	Rev 1.1
Date: Wednesday, August 28, 2013	Sheet 1 of 30	

Revision 1.1

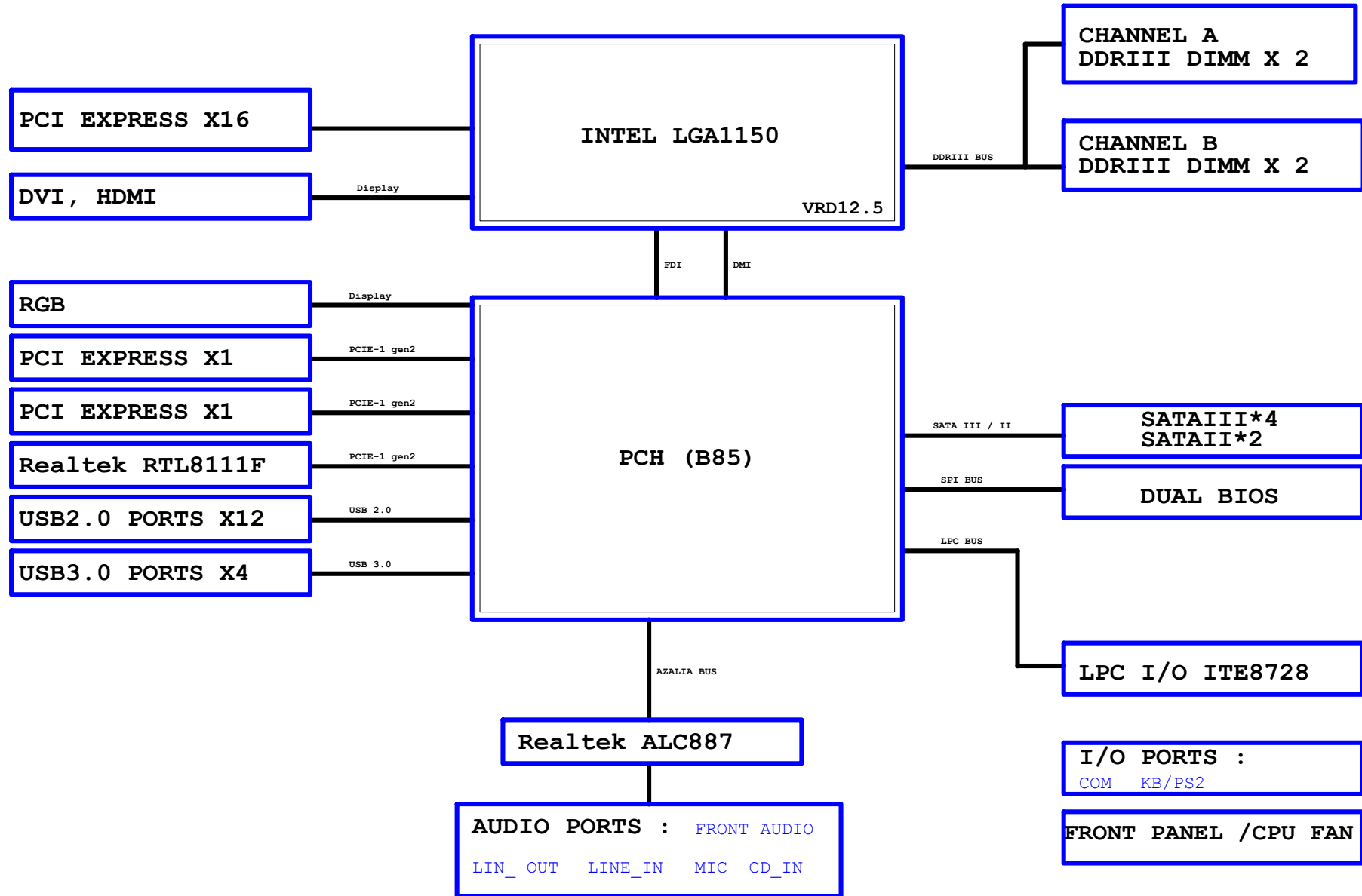
Component value change history

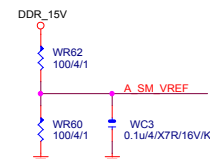
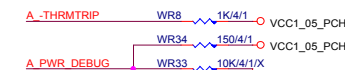
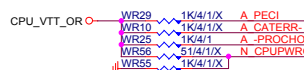
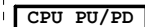
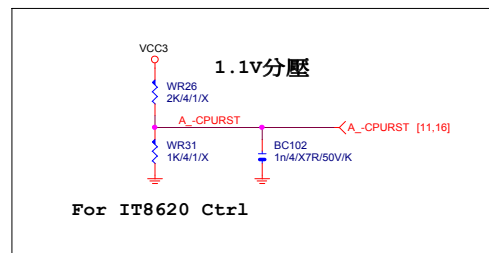
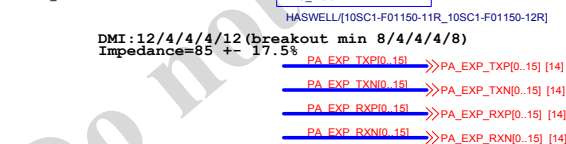
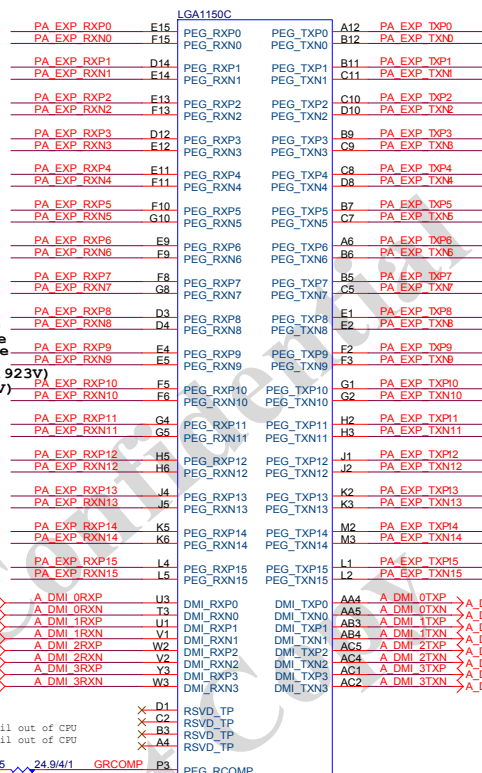
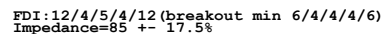
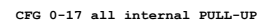
2013/08/23

[illegible]

Circuit or PCB layout change

[illegible]

BLOCK DIAGRAM



LGA1150

(A)

LGA1150

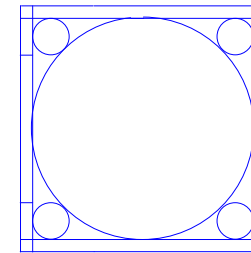
(B)

LGA1150

(CR)

LGA1150A

LGA1150B

CR
CPU RETAINMENT/X

LGA1150_P



ILM_BP/1156/CSP/LM_BP/1156/CSP/[12KRC-0F0001-52R_12KRC-0F0001-51R]

DDR BUS

MAAA0	AU13	DDR0_MA0	DDR0_D00	AD38	MDA0
MAAA1	AV16	DDR0_MA1	DDR0_D01	AD39	MDA1
MAAA2	AU16	DDR0_MA2	DDR0_D02	AF38	MDA2
MAAA3	AW17	DDR0_MA3	DDR0_D03	AF39	MDA3
MAAA4	AU17	DDR0_MA4	DDR0_D04	AD37	MDA4
MAAA5	AW18	DDR0_MA5	DDR0_D05	AD40	MDA5
MAAA6	AW17	DDR0_MA6	DDR0_D06	AE37	MDA6
MAAA7	AT18	DDR0_MA7	DDR0_D07	AF40	MDA7
MAAA8	AU18	DDR0_MA8	DDR0_D08	AH40	MDA9
MAAA9	AT19	DDR0_MA9	DDR0_D09	AH39	MDA13
MAAA10	AW11	DDR0_MA10	DDR0_D10	AK38	MDA10
MAAA11	AV19	DDR0_MA11	DDR0_D11	AK39	MDA11
MAAA12	AU19	DDR0_MA12	DDR0_D12	AH37	MDA12
MAAA13	AY10	DDR0_MA13	DDR0_D13	AH38	MDA14
MAAA14	AT20	DDR0_MA14	DDR0_D14	AK37	MDA15
MAAA15	AU21	DDR0_MA15	DDR0_D15	AK40	MDA15
MODT_A0	AW10	DDR0_ODT0	DDR0_D16	AM40	MDA17
MODT_A1	AY3	DDR0_ODT1	DDR0_D17	AM39	MDA21
MODT_A2	AW9	DDR0_ODT2	DDR0_D18	AP38	MDA18
MODT_A3	AU8	DDR0_ODT3	DDR0_D19	AP39	MDA19
			DDR0_D20	AM37	MDA20
			DDR0_D21	AM38	MDA16
			DDR0_D22	AP37	MDA22
			DDR0_D23	AP40	MDA23
			DDR0_D24	AV37	MDA25
			DDR0_D25	AW37	MDA29
			DDR0_D26	AU35	MDA26
			DDR0_D27	AV35	MDA27
			DDR0_D28	AT37	MDA28
			DDR0_D29	AU37	MDA24
			DDR0_D30	AT35	MDA30
			DDR0_D31	AW35	MDA31
			DDR0_D32	AY6	MDA33
			DDR0_D33	AU6	MDA37
			DDR0_D34	AV4	MDA34
			DDR0_D35	AU4	MDA35
			DDR0_D36	AW6	MDA36
			DDR0_D37	AW4	MDA38
			DDR0_D38	AY4	MDA39
			DDR0_D39	AR1	MDA41
			DDR0_D40	AR4	MDA45
			DDR0_D41	AN3	MDA42
			DDR0_D42	AN4	MDA43
			DDR0_D43	AR2	MDA44
			DDR0_D44	AR3	MDA40
			DDR0_D45	AN2	MDA46
			DDR0_D46	AN1	MDA47
			DDR0_D47	AL1	MDA49
			DDR0_D48	AL4	MDA53
			DDR0_D49	AL3	MDA50
			DDR0_D50	AJ4	MDA51
			DDR0_D51	AL2	MDA52
			DDR0_D52	AL3	MDA48
			DDR0_D53	AJ2	MDA54
			DDR0_D54	AJ1	MDA55
			DDR0_D55	AG1	MDA57
			DDR0_D56	AG4	MDA61
			DDR0_D57	AE3	MDA58
			DDR0_D58	AE4	MDA59
			DDR0_D59	AG2	MDA60
			DDR0_D60	AG3	MDA56
			DDR0_D61	AE2	MDA62
			DDR0_D62	AE1	MDA63
			DDR0_D63	AE39	DQSA0
			DDR0_D64	AJ39	DQSA1
			DDR0_D65	AN39	DQSA2
			DDR0_D66	AV36	DQSA3
			DDR0_D67	AV5	DQSA4
			DDR0_D68	AP3	DQSA5
			DDR0_D69	AK3	DQSA6
			DDR0_D70	AF3	DQSA7
			DDR0_D71	AV32	DQSA0
			DDR0_D72	AE38	DQSA1
			DDR0_D73	AJ38	DQSA2
			DDR0_D74	AN38	DQSA3
			DDR0_D75	AJ36	DQSA4
			DDR0_D76	AW5	DQSA4
			DDR0_D77	AP2	DQSA5
			DDR0_D78	AK2	DQSA6
			DDR0_D79	AF2	DQSA7
			DDR0_D80	AU32	

HASWELL-[10SC1-F01150-11R_10SC1-F01150-12R]

[8] SBAB0	←	SBAB0	AK17
[8] SBAB1	←	SBAB1	AK18
[8] SBAB2	←	SBAB2	AK19

[8] CKEB0	←	CKEB0	AW29
[8] CKEB1	←	CKEB1	AY29
[8] CKEB2	←	CKEB2	AU28
[8] CKEB3	←	CKEB3	AU29

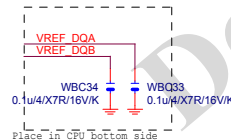
[8] -CSB0	←	-CSB0	AP17
[8] -CSB1	←	-CSB1	AN15
[8] -CSB2	←	-CSB2	AN17
[8] -CSB3	←	-CSB3	AL15

[8] DCLKB0	←	DCLKB0	AM20
[8] DCLKB1	←	DCLKB1	AM21
[8] DCLKB2	←	DCLKB2	AP22
[8] DCLKB3	←	DCLKB3	AP21

[8] DCLKB2	←	DCLKB2	AN20
[8] DCLKB3	←	DCLKB3	AN21
[8] DCLKB4	←	DCLKB4	AP19
[8] DCLKB5	←	DCLKB5	AP20

[8] -SCASB	←	-SCASB	AP18
[8] -SRASB	←	-SRASB	AM18
[8] -SWEB	←	-SWEB	AK18

[7] VREF_D0A	←	VREF_D0A	AB39
[8] VREF_D0B	←	VREF_D0B	AB40



HASWELL-[10SC1-F01150-11R_10SC1-F01150-12R]

[7] MODT_A[0..3] ← MODT_A[0..3]

[8] MODT_B[0..3] ← MODT_B[0..3]

[7] MDA[0..63] ← MDA[0..63]

[8] MDB[0..63] ← MDB[0..63]

[7] DQSA[0..7] ← DQSA[0..7]

[7] -DQSA[0..7] ← -DQSA[0..7]

[7] MAA[0..15] ← MAA[0..15]

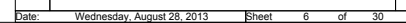
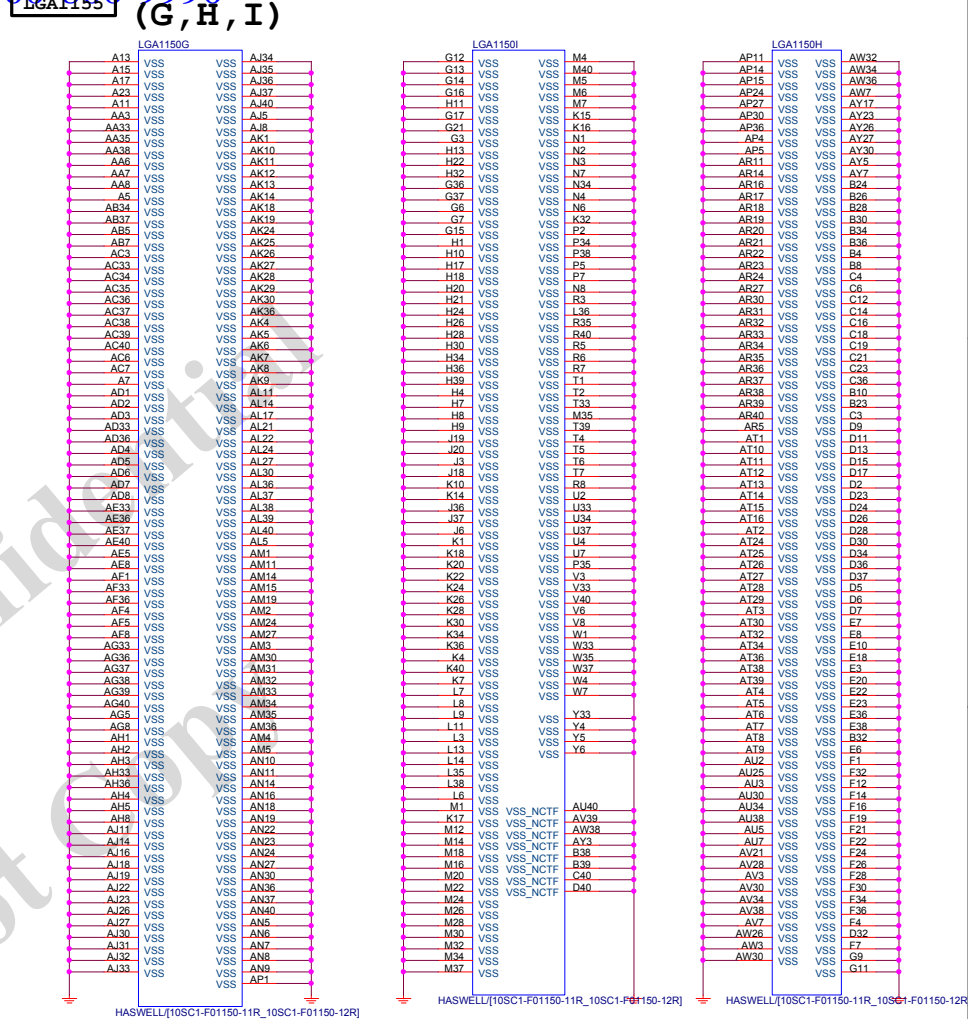
[8] MAB[0..15] ← MAB[0..15]

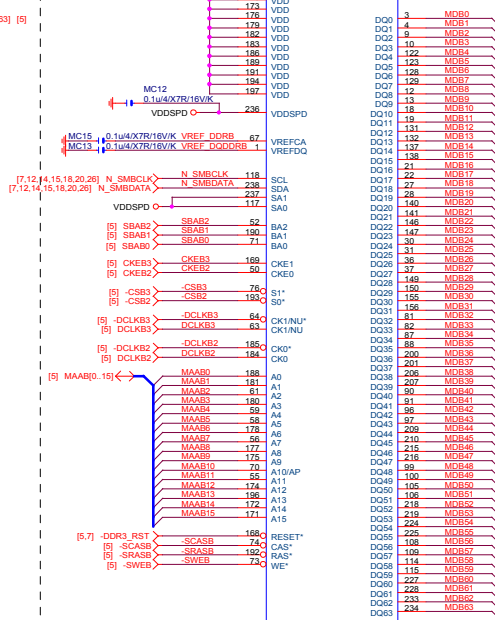
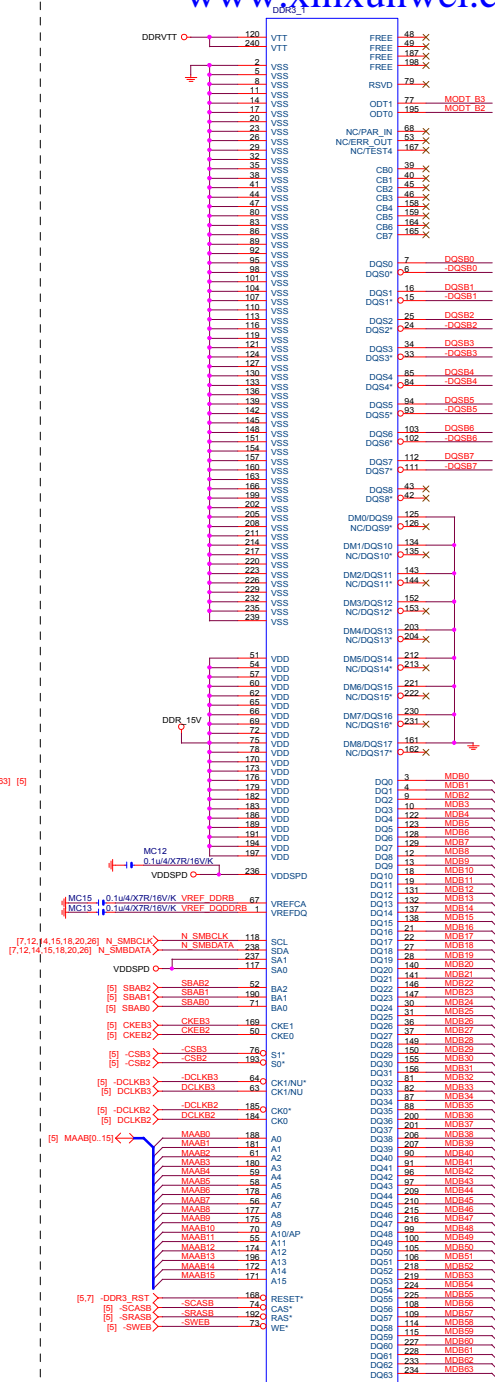
[8] DQSB[0..7] ← DQSB[0..7]

[8] -DQSB[0..7] ← -DQSB[0..7]

Gigabyte Technology

Title			CPU LGA1150-B
Size	Document Number	Rev	1.1
Custom	GA-B85M-DS3H		
Date:	Wednesday, August 28, 2013	Sheet	5 of 30

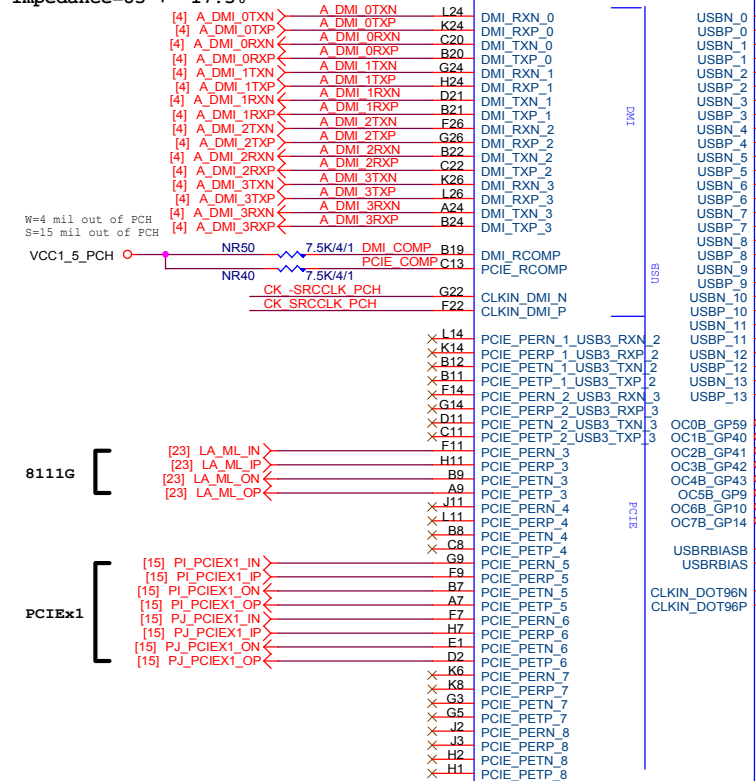




Date: _____ Sheet 8 of 30

PCH (B)

DMI:12/4/4/4/12 (breakout min 8/4/4/4/8)
Impedance=85 +- 17.5%



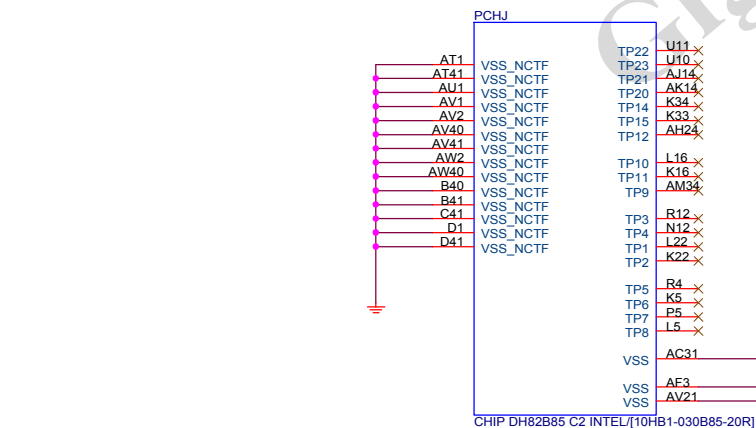
放靠近 Device & PCI-E Slot
Impedance=80 +- 17.5%

```

_PCIEX1:16/5/5/5/16_(breakout_min_8/4/4/4/8)

```

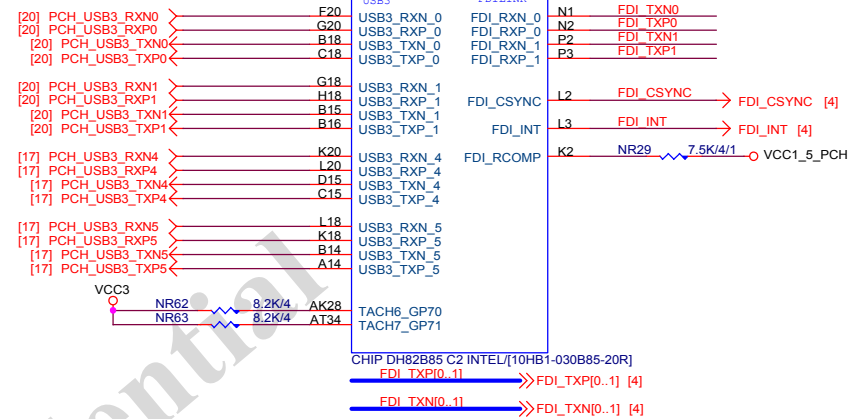
PCH (J)



CHIP DH82B85 C2 INTEL/[10HB1-030B85-20R]

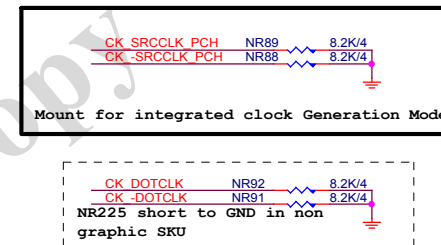
PCH (F)

B85: Port 6/7 N/A
H81: Port 6/7/12/13 N/A



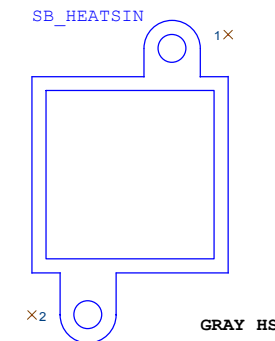
USB3.0:20/5/7/5/20 (breakout min
8/4/4/4/8) ; ONLY 3 VIAS
Impedance=85 +- 17.5%
Back Panel < 10000 MILS
Front Panel < 6000 MILS

PCH	CLK	PD
-----	-----	----



PCH H/S

LOW COST ICH7 HEATSINK



PCH_HS
PCH_HS/[12SP2-030005-43R_12SP2-030005-41R_12SP2-030005-42R

USB TABLE

```
OC[3:0]# for Device 29 (ports 0-7)
OC[7:4]# for Device 26 (ports 8-13)
```

USB OC# Configure	
OC0#	F_USB30
OC1#	USB_LAN
OC2#	R_USB30
OC3#	N/A
OC4#	F_USB1
OC5#	F_USB2
OC6#	KB_MS_USB
OC7#	Not Use

Gigabyte Technology

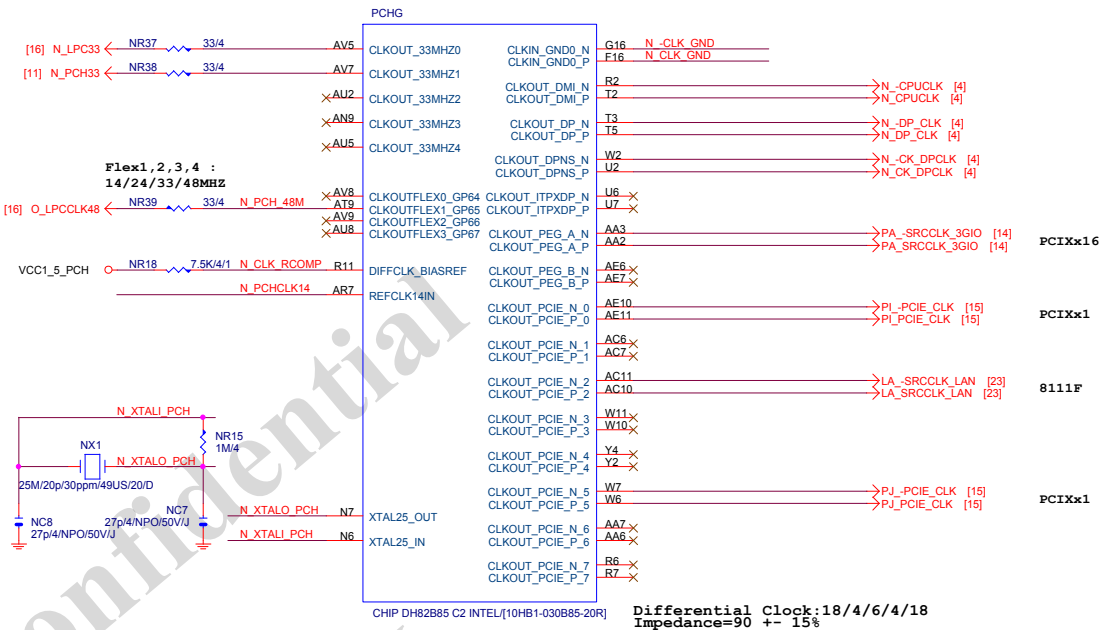
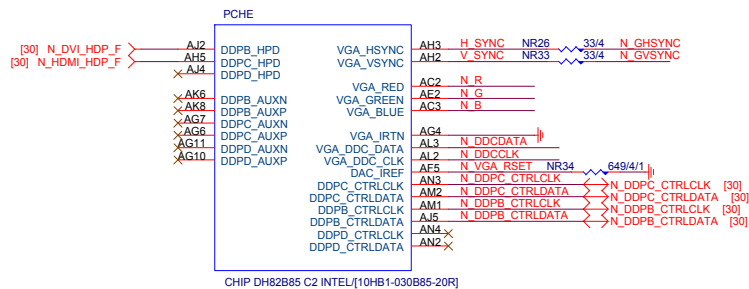
Title			
PCH FDI,DMI,USB ,PCIE,NVRAM			
Size	Document Number		Rev
Custom	GA-B85M-DS3H		1.1
Date:	Wednesday, August 28, 2013	Sheet	9 of 30

PCH

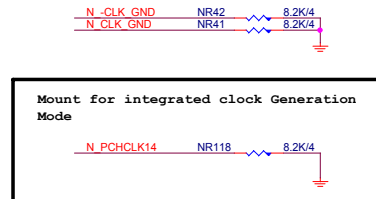
(E)

PCH

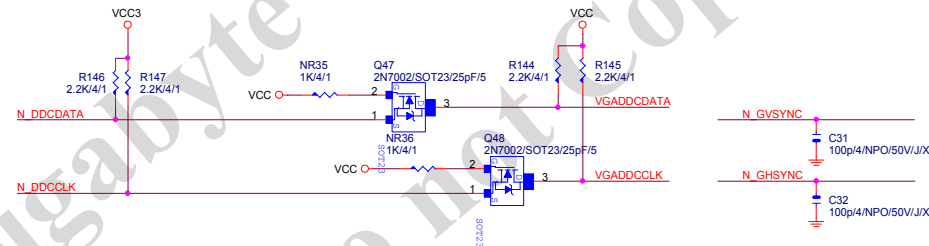
(G)



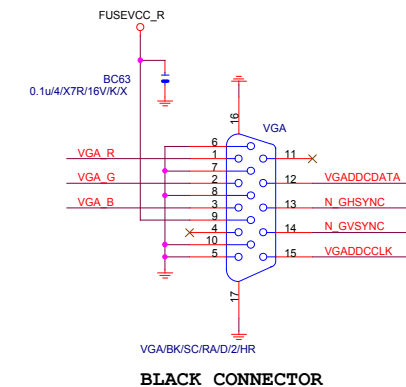
PCH CLK PD



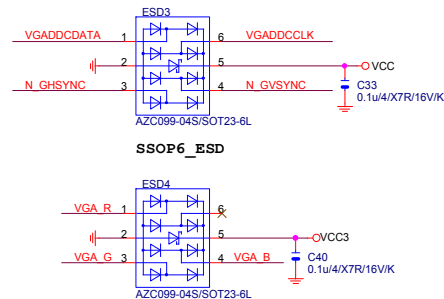
VGA DDC



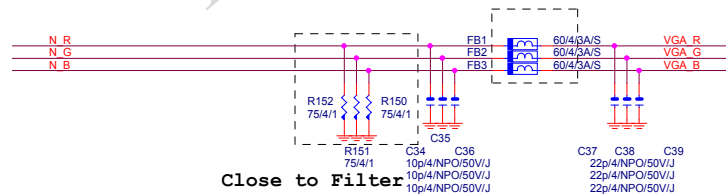
VGA CONNECTOR



VGA ESD

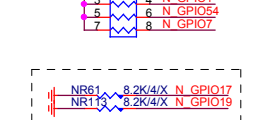
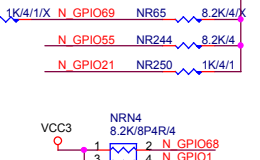
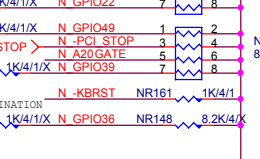
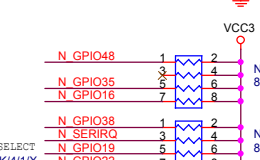
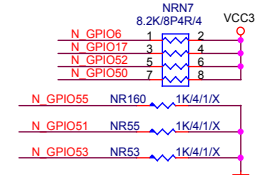
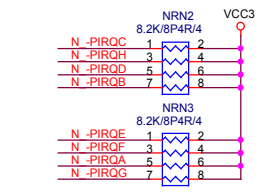
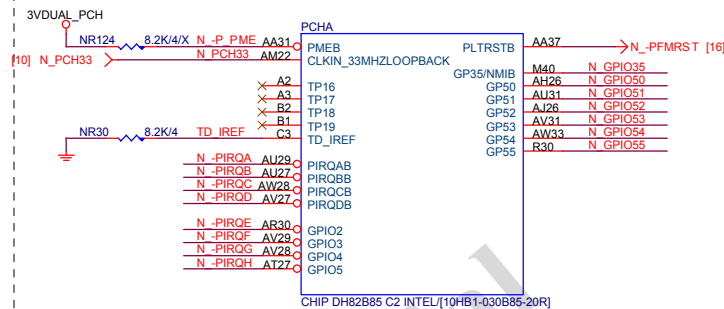


VGA DDC



Gigabyte Technology

Title		
PCH DISPLAY_CLK BUFFER		
Size Custom		
Document Number		
GA-B85M-DS3H		
Date: Wednesday, August 28, 2013		
Sheet 10 of 30		
Rev 1.1		



N SATA0TXP	0.01u4/X7R/25V/K	NC44	N SATA0TXPC	1	GND
N SATA0TXN	0.01u4/X7R/25V/K	NC43	N SATA0TXNC	2	T+
				3	T-
N SATA0RXN	0.01u4/X7R/25V/K	NC38	N SATA0RXNC	4	GND
N SATA0RXP	0.01u4/X7R/25V/K	NC37	N SATA0RXP	5	R-
				6	R+

N SATA2TXP	0.01u4/X7R/25V/K	NC36	N SATA2TXPC	1	GND
N SATA2TXN	0.01u4/X7R/25V/K	NC35	N SATA2TXNC	2	T+
				3	T-
				4	GND
N SATA2RXN	0.01u4/X7R/25V/K	NC30	N SATA2RXNC	5	R-
N SATA2RXP	0.01u4/X7R/25V/K	NC29	N SATA2RXP	6	R+
				7	GND

N SATA4TXP	NC45	0.01u4/X7R/25V/K	N SATA4TXPC	1	GND
N SATA4TXN	NC46	0.01u4/X7R/25V/K	N SATA4TXNC	2	T+
				3	T-
N SATA4RXN	NC47	0.01u4/X7R/25V/K	N SATA4RXNC	4	GND
N SATA4RXP	NC48	0.01u4/X7R/25V/K	N SATA4RXP	5	R-
				6	R+

5	
---	--

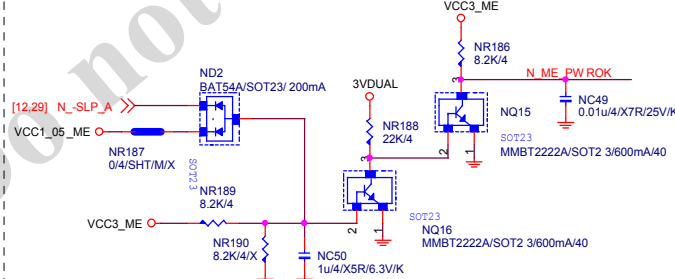
N_SATA1TXP	0.01u4/X7R/25V/K	NC42	N_SATA1TXPC	1	GND
N_SATA1TXN	0.01u4/X7R/25V/K	NC41	N_SATA1TXNC	2	T+
				3	T-
				4	GND
N_SATA1RXN	0.01u4/X7R/25V/K	NC40	N_SATA1RXNC	5	R-
N_SATA1RXP	0.01u4/X7R/25V/K	NC39	N_SATA1RXP	6	R+
				7	

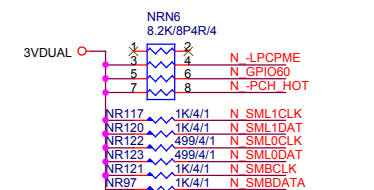
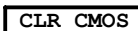
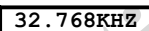
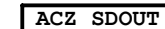
N_SATA3TXP	0.01u4/X7R/25V/K	NC34	N_SATA3TXPC	1	GND
N_SATA3TXN	0.01u4/X7R/25V/K	NC33	N_SATA3TXNC	2	T+
				3	T-
				4	
N_SATA3RXN	0.01u4/X7R/25V/K	NC32	N_SATA3RXNC	5	GND
N_SATA3RXP	0.01u4/X7R/25V/K	NC31	N_SATA3RXP	6	R-
				7	R+
					GND

N_SATA5TXP	NC57	0.01u4/X7R/25V/K	N_SATA5XPC	1	GND
N_SATA5TXN	NC56	0.01u4/X7R/25V/K	N_SATA5XNC	2	T+
				3	T-
N_SATA5RXN	NC55	0.01u4/X7R/25V/K	N_SATA5R_XNC	4	GND
N_SATA5RXP	NC54	0.01u4/X7R/25V/K	N_SATA5RXPC	5	R-
				6	R+

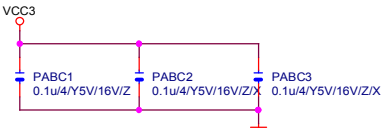
4	
---	--

```
GPIO37 PU VCC3 ENABLE SBA
For H87&B85
```

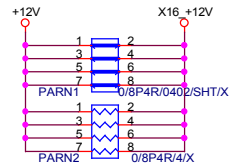




PCIEX16 CAP



PCIEX16 PROTECT SHT



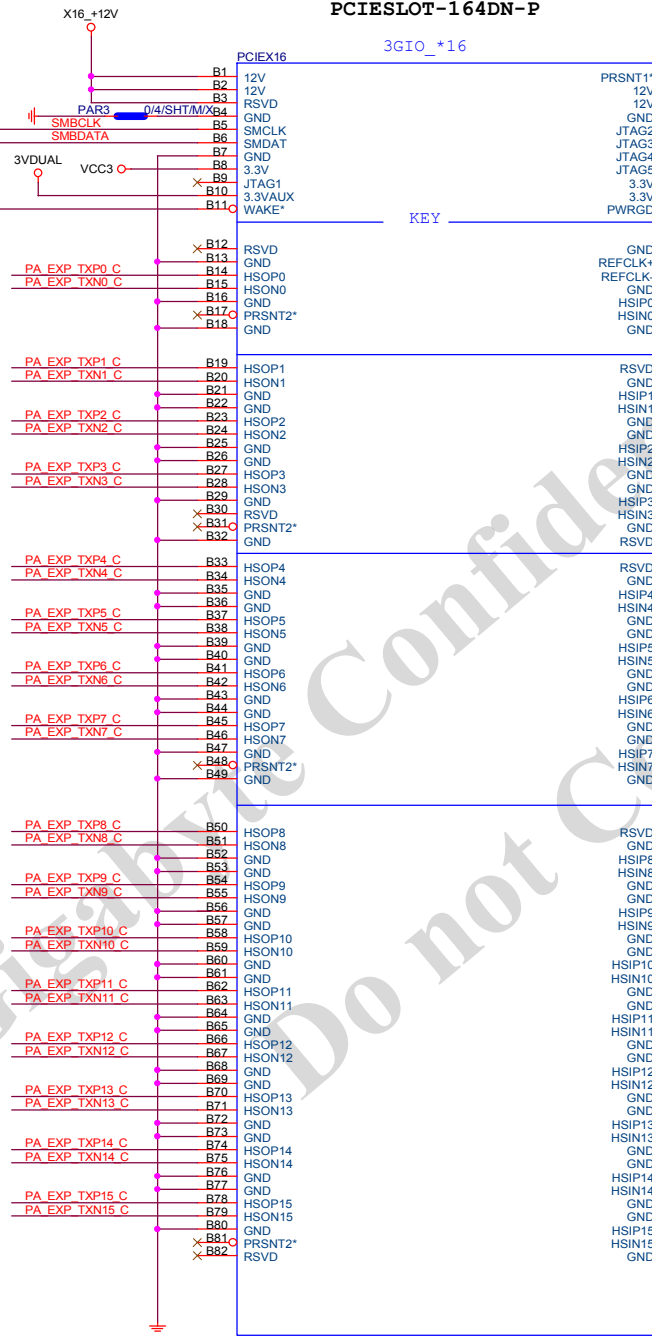
PCIEX16 AC CAP

PA EXP TXP0	PAC5	0.22u4/X5R/6.3V/K	PA EXP TXP0 C
PA EXP TXN0	PAC4	0.22u4/X5R/6.3V/K	PA EXP TXN0 C
PA EXP TXP1	PAC6	0.22u4/X5R/6.3V/K	PA EXP TXP1 C
PA EXP TXN1	PAC7	0.22u4/X5R/6.3V/K	PA EXP TXN1 C
PA EXP TXP2	PAC8	0.22u4/X5R/6.3V/K	PA EXP TXP2 C
PA EXP TXN2	PAC9	0.22u4/X5R/6.3V/K	PA EXP TXN2 C
PA EXP TXP3	PAC10	0.22u4/X5R/6.3V/K	PA EXP TXP3 C
PA EXP TXN3	PAC11	0.22u4/X5R/6.3V/K	PA EXP TXN3 C
PA EXP TXP4	PAC12	0.22u4/X5R/6.3V/K	PA EXP TXP4 C
PA EXP TXN4	PAC13	0.22u4/X5R/6.3V/K	PA EXP TXN4 C
PA EXP TXP5	PAC14	0.22u4/X5R/6.3V/K	PA EXP TXP5 C
PA EXP TXN5	PAC15	0.22u4/X5R/6.3V/K	PA EXP TXN5 C
PA EXP TXP6	PAC16	0.22u4/X5R/6.3V/K	PA EXP TXP6 C
PA EXP TXN6	PAC17	0.22u4/X5R/6.3V/K	PA EXP TXN6 C
PA EXP TXP7	PAC19	0.22u4/X5R/6.3V/K	PA EXP TXP7 C
PA EXP TXN7	PAC18	0.22u4/X5R/6.3V/K	PA EXP TXN7 C
PA EXP TXP8	PAC20	0.22u4/X5R/6.3V/K	PA EXP TXP8 C
PA EXP TXN8	PAC21	0.22u4/X5R/6.3V/K	PA EXP TXN8 C
PA EXP TXP9	PAC22	0.22u4/X5R/6.3V/K	PA EXP TXP9 C
PA EXP TXN9	PAC23	0.22u4/X5R/6.3V/K	PA EXP TXN9 C
PA EXP TXP10	PAC24	0.22u4/X5R/6.3V/K	PA EXP TXP10 C
PA EXP TXN10	PAC25	0.22u4/X5R/6.3V/K	PA EXP TXN10 C
PA EXP TXP11	PAC26	0.22u4/X5R/6.3V/K	PA EXP TXP11 C
PA EXP TXN11	PAC27	0.22u4/X5R/6.3V/K	PA EXP TXN11 C
PA EXP TXP12	PAC28	0.22u4/X5R/6.3V/K	PA EXP TXP12 C
PA EXP TXN12	PAC29	0.22u4/X5R/6.3V/K	PA EXP TXN12 C
PA EXP TXP13	PAC30	0.22u4/X5R/6.3V/K	PA EXP TXP13 C
PA EXP TXN13	PAC31	0.22u4/X5R/6.3V/K	PA EXP TXN13 C
PA EXP TXP14	PAC32	0.22u4/X5R/6.3V/K	PA EXP TXP14 C
PA EXP TXN14	PAC33	0.22u4/X5R/6.3V/K	PA EXP TXN14 C
PA EXP TXP15	PAC34	0.22u4/X5R/6.3V/K	PA EXP TXP15 C
PA EXP TXN15	PAC35	0.22u4/X5R/6.3V/K	PA EXP TXN15 C

PA EXP RXIP0.15] >>PA_EXP_RXIP[0.15] [4]
PA EXP RXN0.15] >>PA_EXP_RXN[0.15] [4]
PA EXP TXIP0.15] >>PA_EXP_TXIP[0.15] [4]
PA EXP TXN0.15] >>PA_EXP_TXN[0.15] [4]

PCIEX16 SLOT

[7,8,12,15,18,20,26] N_SMBCLK
[7,8,12,15,18,20,26] N_SMBDATA
[12,15,23] N_PCIE_WAKE



PCI EXPRESS 164P/16X/BLACK LOT/[11AC1-021164-C1R]

BLACK CONNECTOR

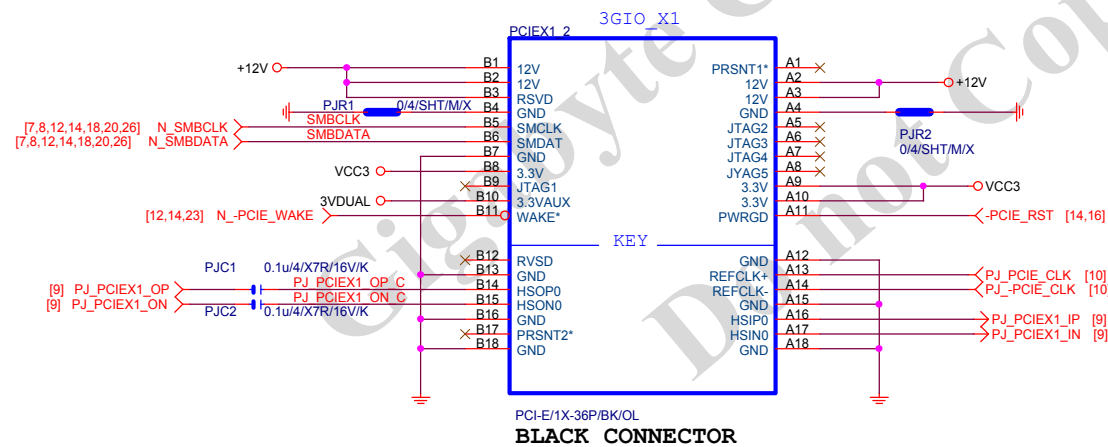
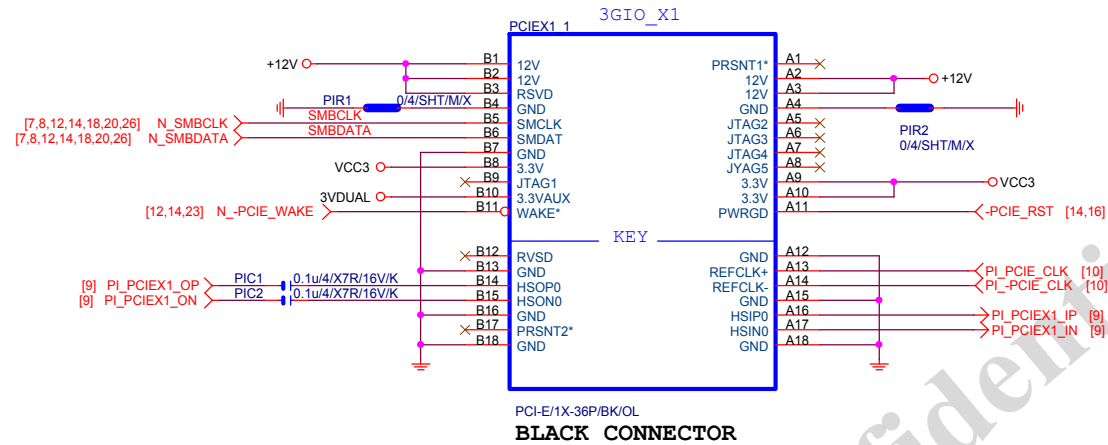
www.xinxunwei.com 400-800-9990

PCIESLOT-164DN-P

Gigabyte Technology

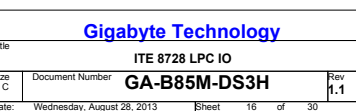
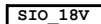
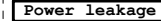
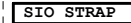
Title		
PCI EXPRESS * 16		
Size	Document Number	Rev
Custom	GA-B85M-DS3H	1.1
Date:	Wednesday, August 28, 2013	Sheet 14 of 30

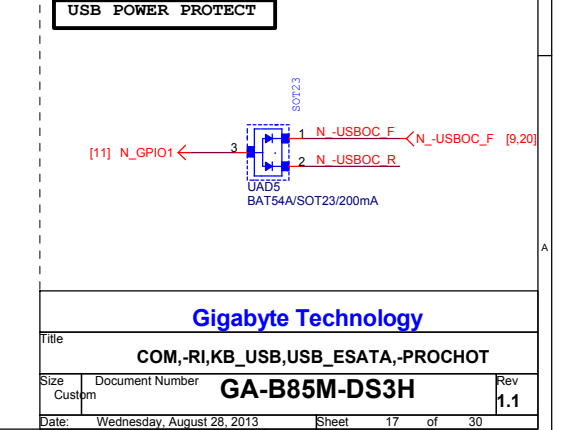
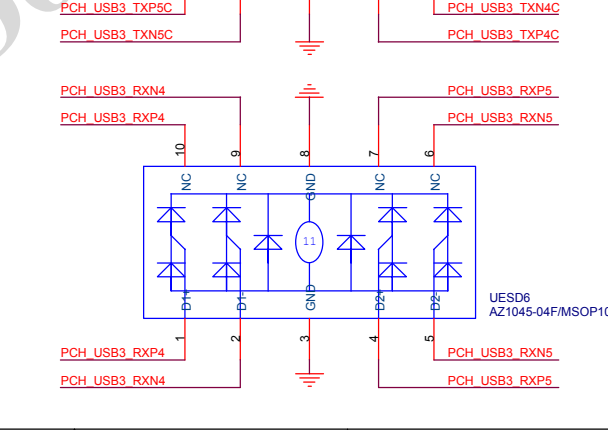
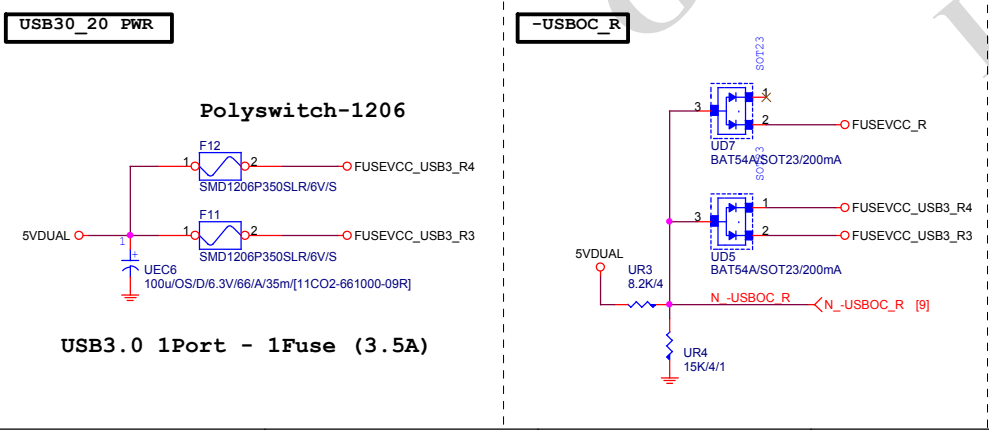
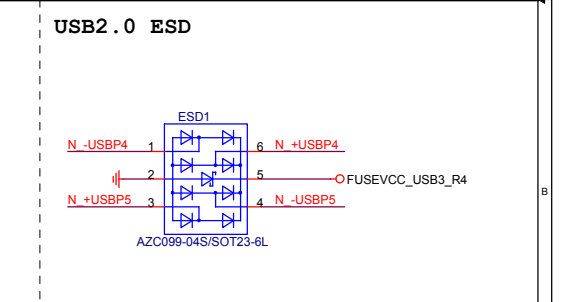
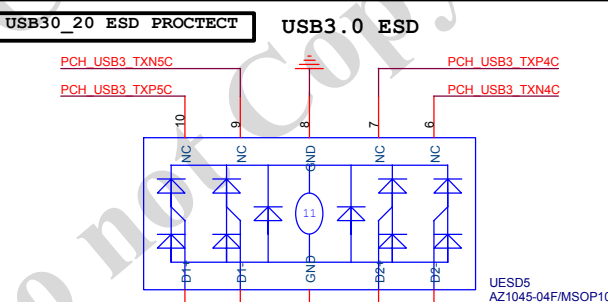
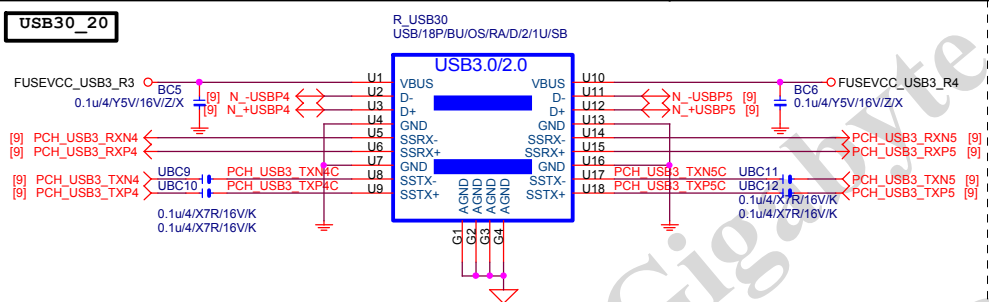
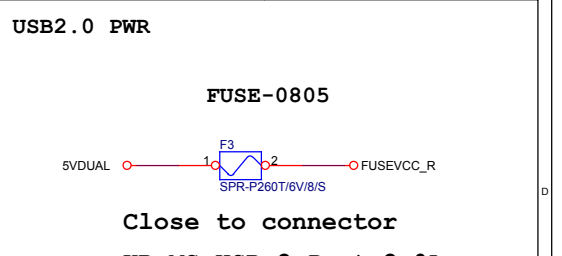
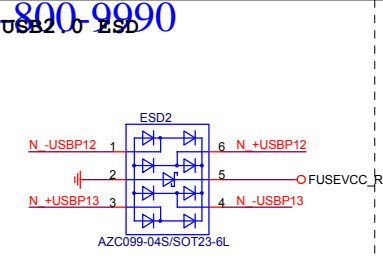
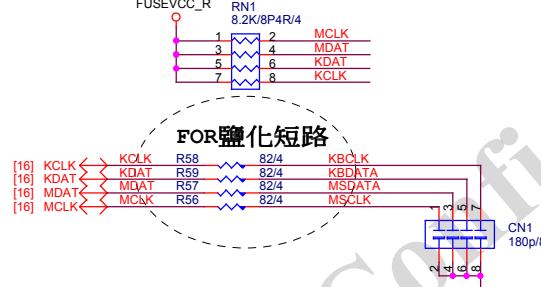
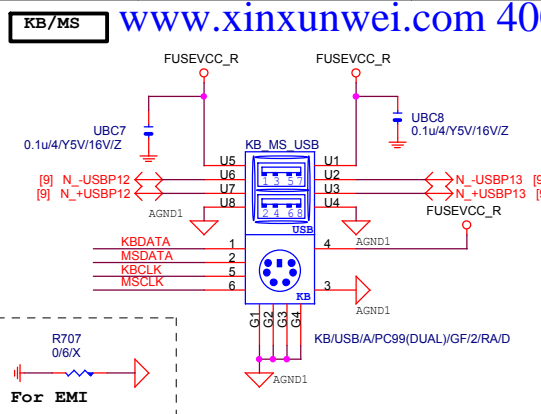
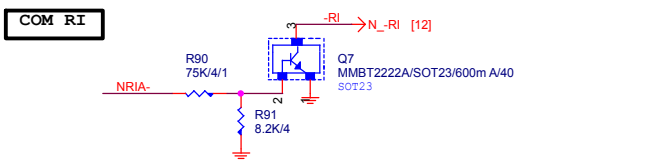
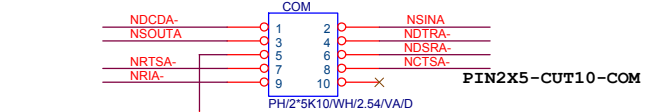
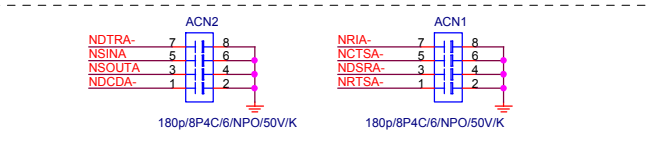
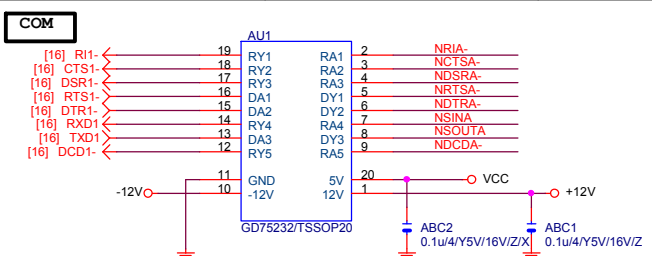
PCIEX1 SLOT



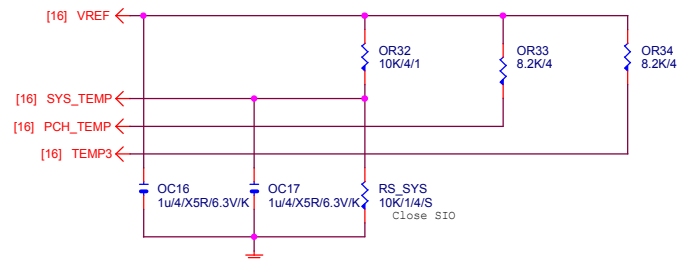
Gigabyte Technology

Title			PCI EXPRESS X 1 PORT
Size	Document Number	GA-B85M-DS3H	
Custom			Rev 1.1
Date:	Wednesday, August 28, 2013	Sheet	15 of 30

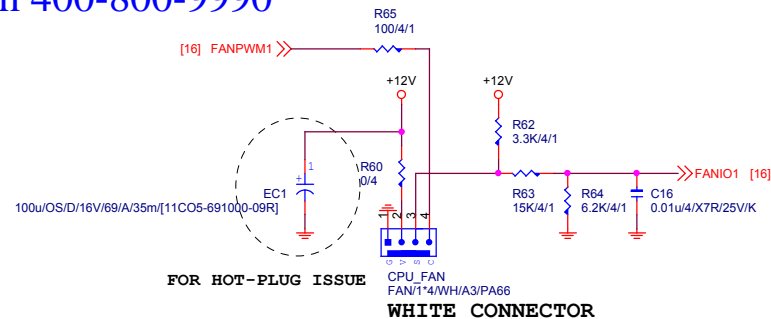




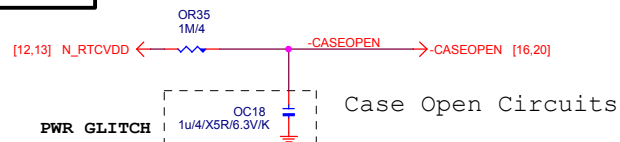
TEMP H/W MONITOR



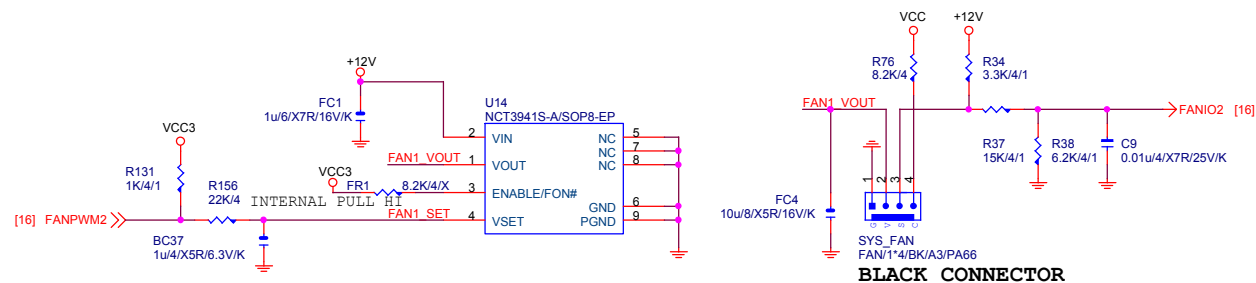
CPU SMART FAN



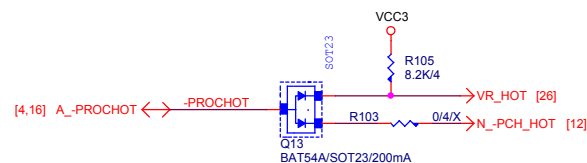
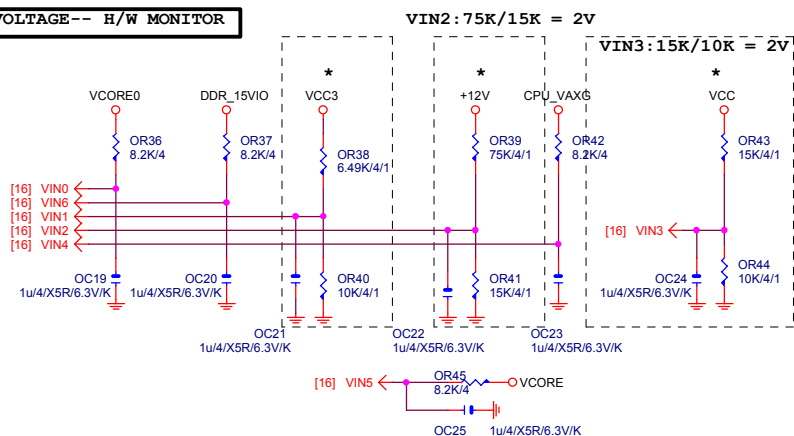
CASE OPEN



SYS SMART FAN

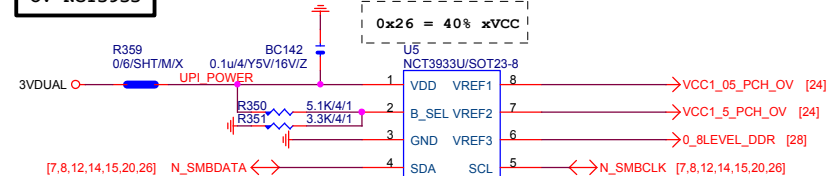


VOLTAGE-- H/W MONITOR



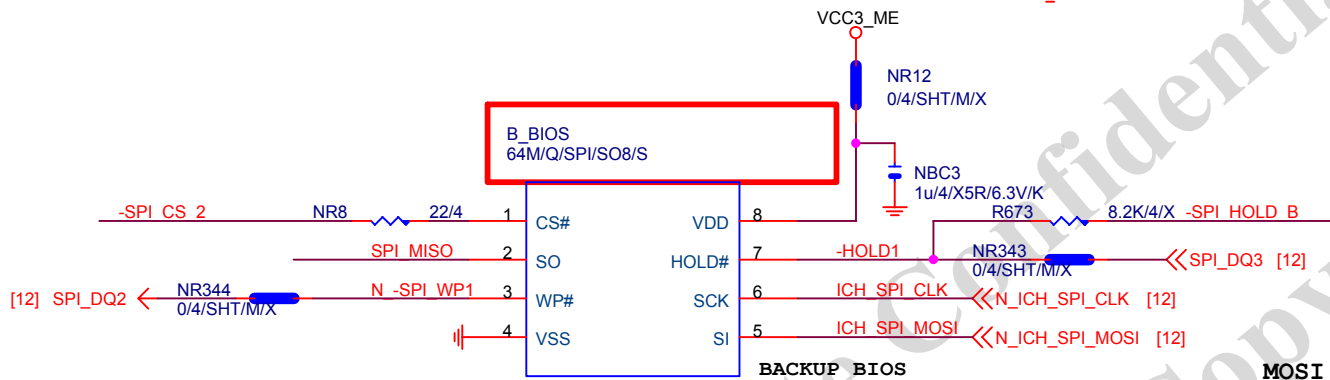
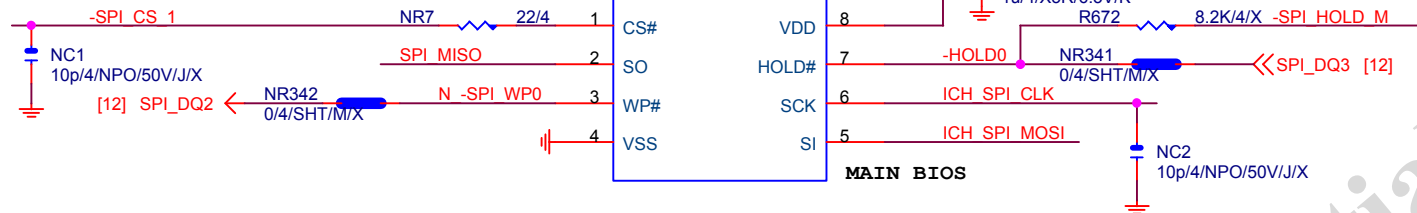
接pwm feedback pin

OV NCT3933



Gigabyte Technology

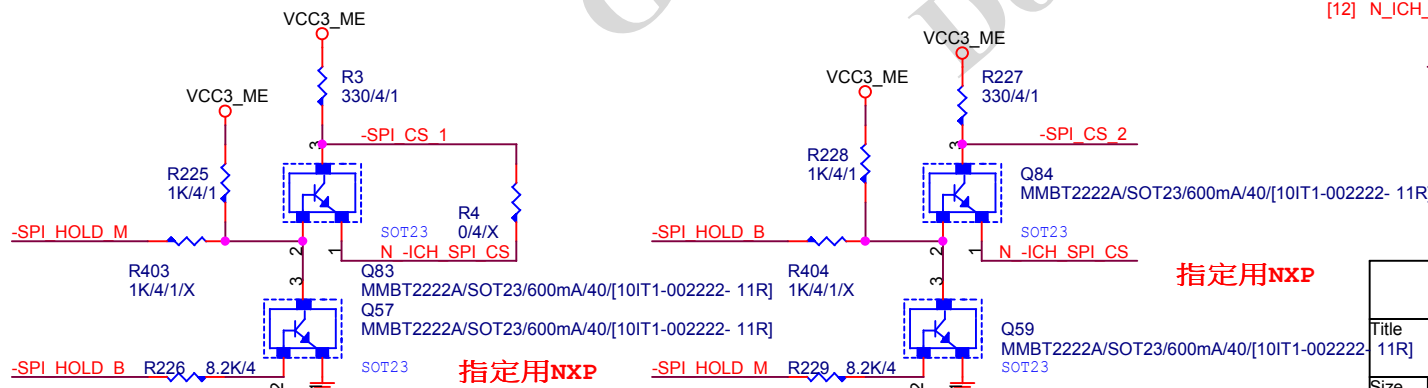
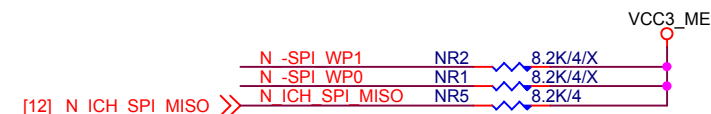
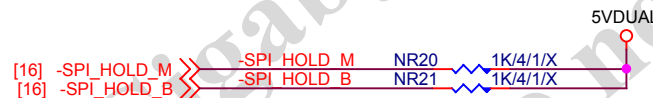
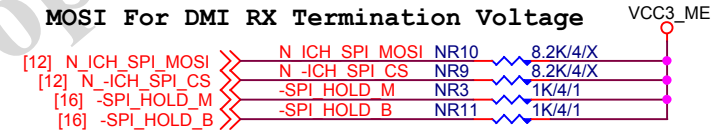
Title			HWM,FAN CTRL,OV	
Size			Document Number	
Custom			GA-B85M-DS3H	
Date:			Wednesday, August 28, 2013	Sheet 18 of 30
			Rev	1.1



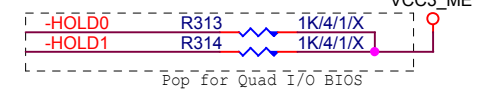
BOOT DEVICE	GNT0	GNT1
LPC	0	0
PCI	0	1
NAND	1	0
SPI	1	1

1 means floating
0 means PD 1K

MOSI For DMI RX Termination Voltage



CHECK



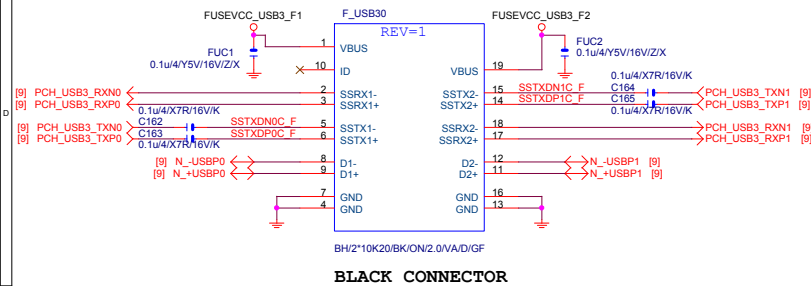
Gigabyte Technology

DUAL BIOS

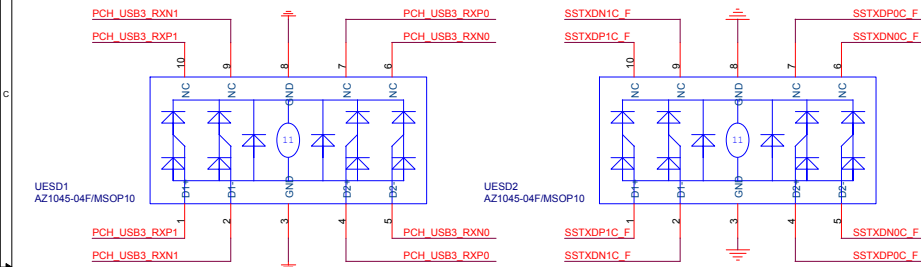
GA-B85M-DS3H

Title	11R	Rev	1.1
Size	Custom	Document Number	
Date	Wednesday, August 28, 2013	Sheet	19 of 30

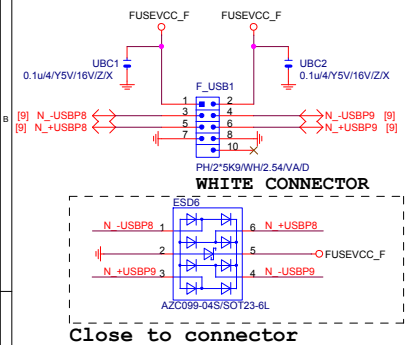
F_USB30



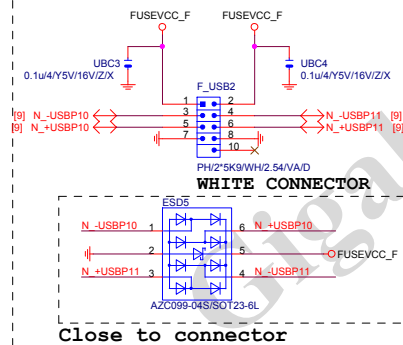
F_USB30 ESD PROTECT



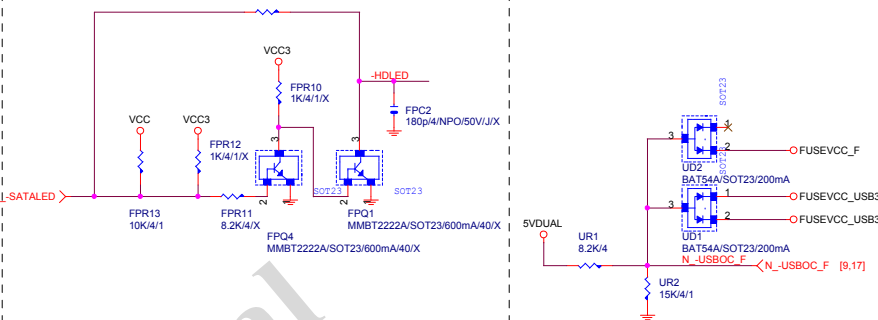
FRONT USB1



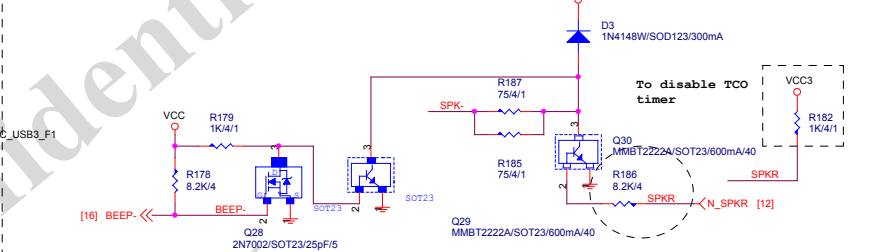
FRONT USB2



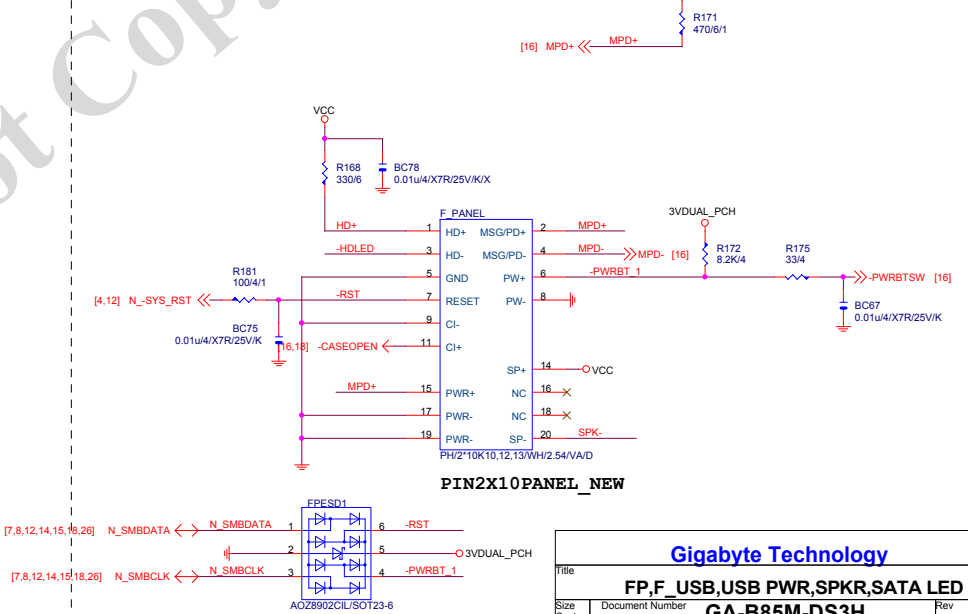
0080
SATA LTD



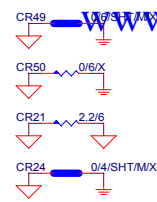
SPKR



INTEL FRONT PANEL



[illegible]

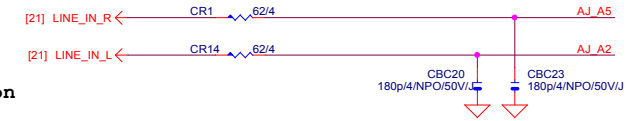


www.xinxunwei.com 400-800-9990

LINE-IN

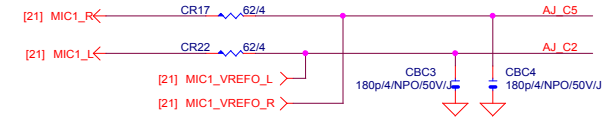
Verify MIC function
in LINE-in

Only reserved for ALC888

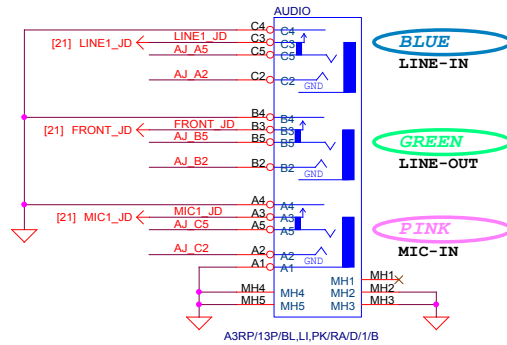


For 889A/888

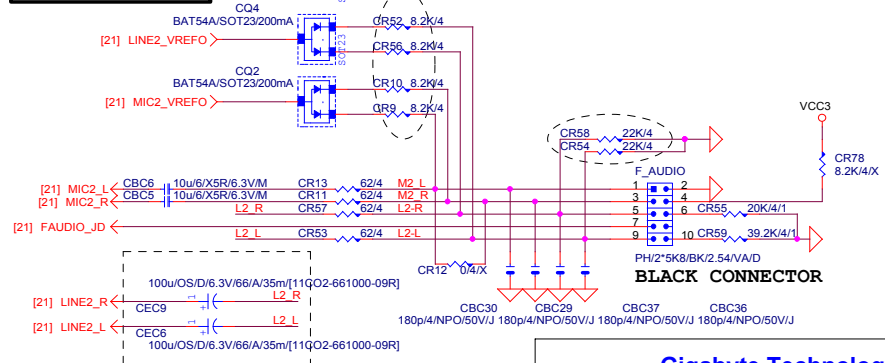
MIC-IN



SPDIF_OUT



AZALIA FRONT PANEL

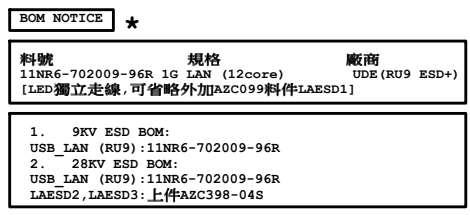
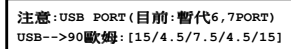
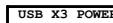


Gigabyte Technology

Title			
AUDIO JACK			
Size	Document Number	Rev	
Custom	GA-B85M-DS3H	1.1	
Date:	Wednesday, August 28, 2013	Sheet	22 of 30



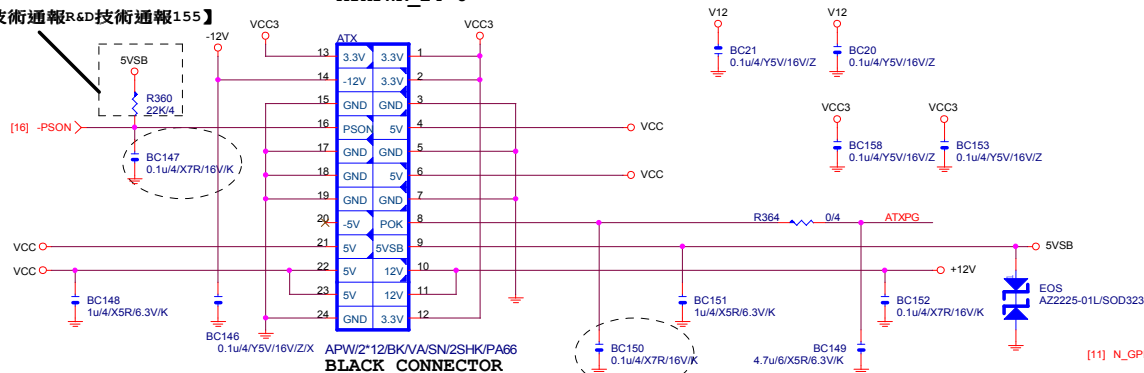
	RTL8111E
AVDD33	3.3V
DVDD33	3.3V
VDDREG	3.3V
DVDD10	1.05V



ATXX24 POWER CONNECTOR

【技術通報R&D技術通報155】

ATXPWR_24-6



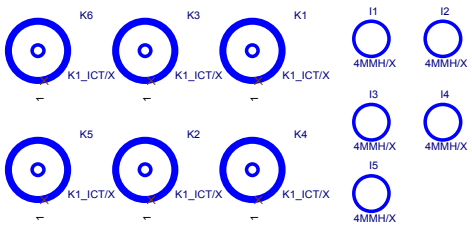
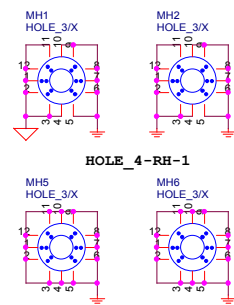
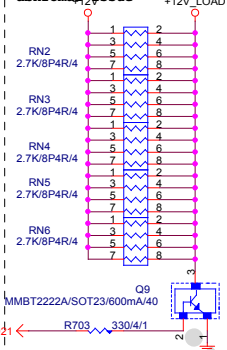
www.xinxunwei.com 400-800-0900

ATXX4 POWER CONNECTOR

ATXPW2X4-6

【技術通報R&D技術通報155】

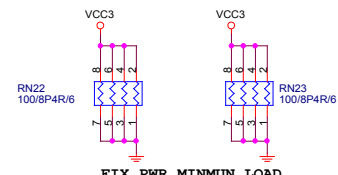
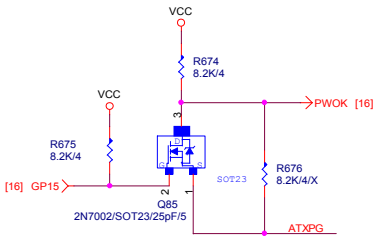
To fix 12V light load abnormal issue



To prevent the 5VSB under loading when boot

PWOK PATCH

【技術通報R&D技術通報154】



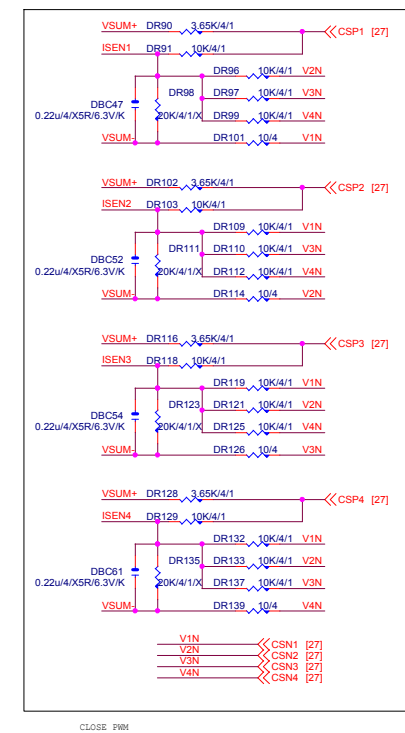
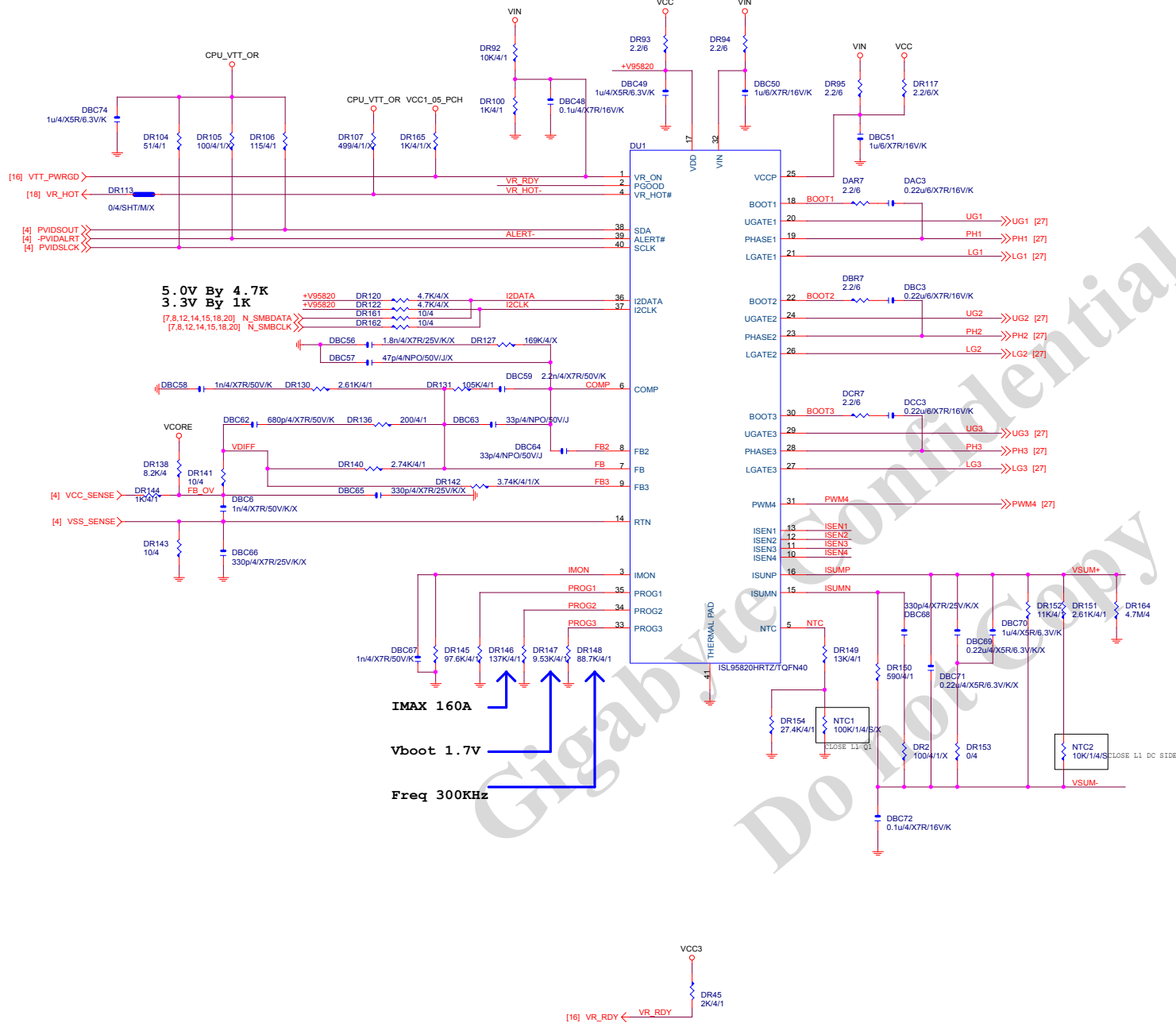
Gigabyte Technology

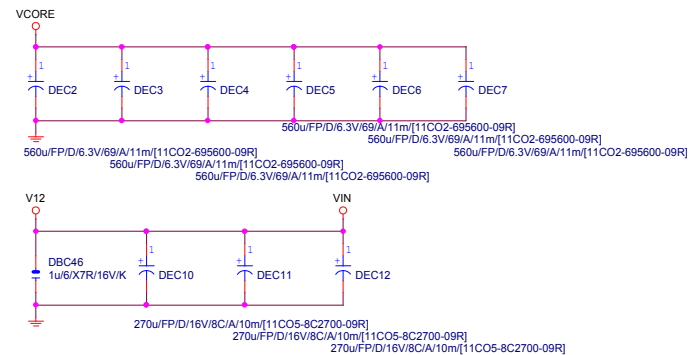
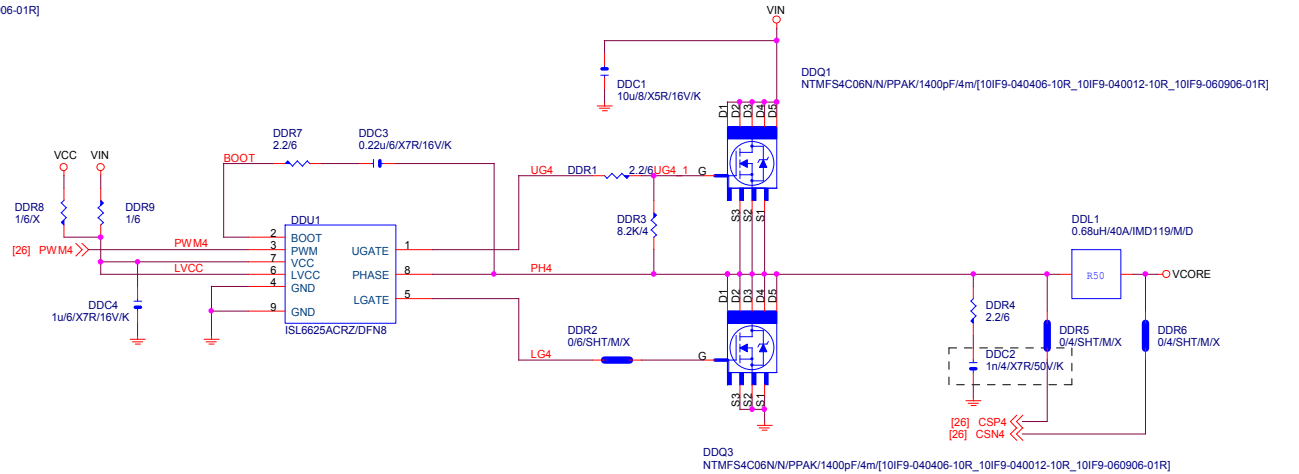
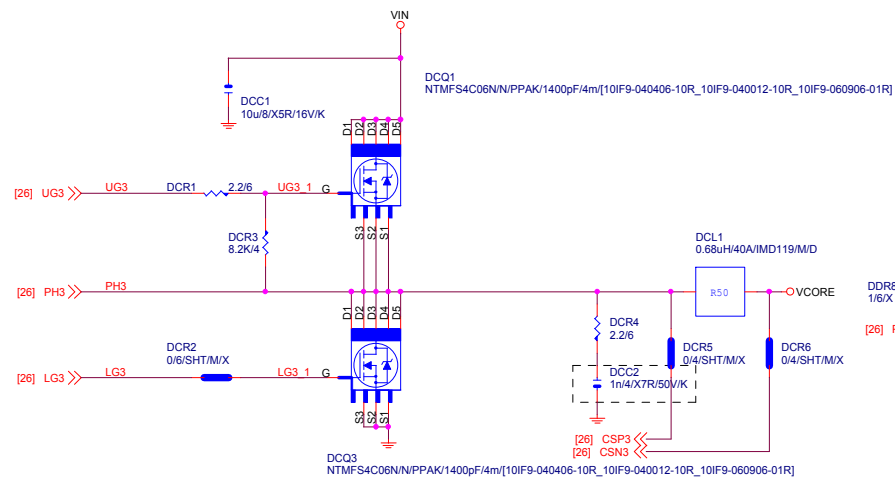
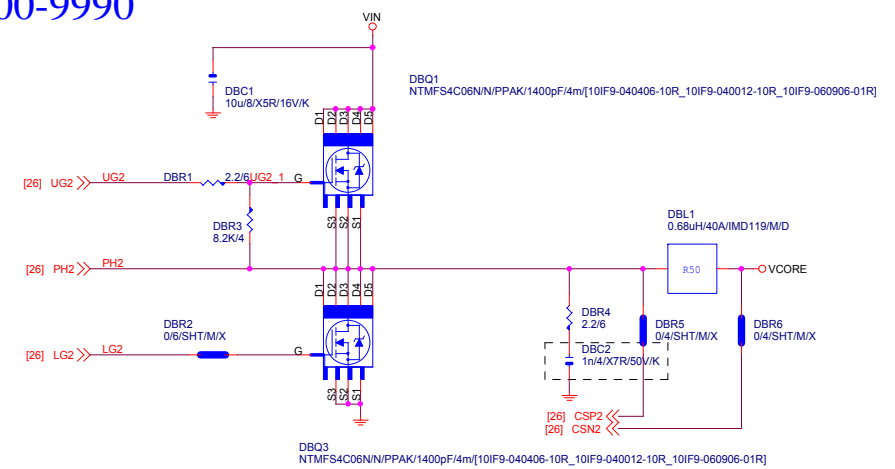
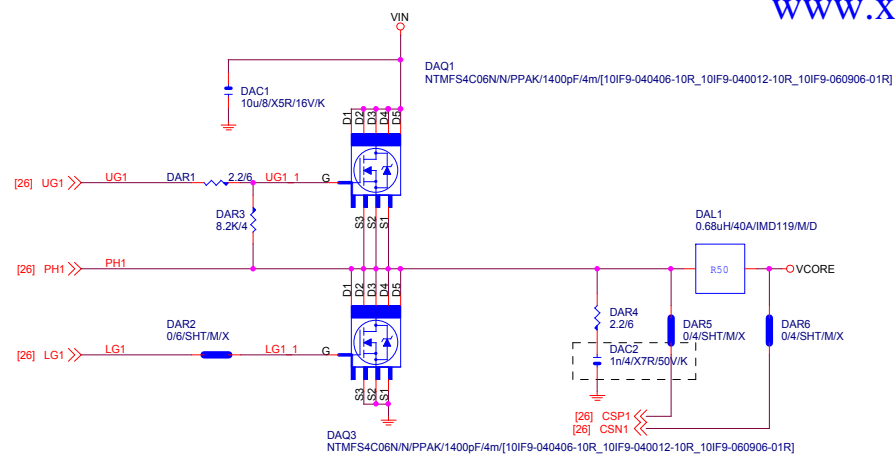
ATX CONNECTOR

GA-B85M-DS3H

Rev 1.1

Date: Wednesday, August 28, 2013 Sheet 25 of 30





Gigabyte Technology

Title	CPU CORE VR-2		
Size	Document Number	GA-B85M-DS3H	
Custom		Rev	1.1
Date	Wednesday, August 28, 2013	Sheet	27 of 30

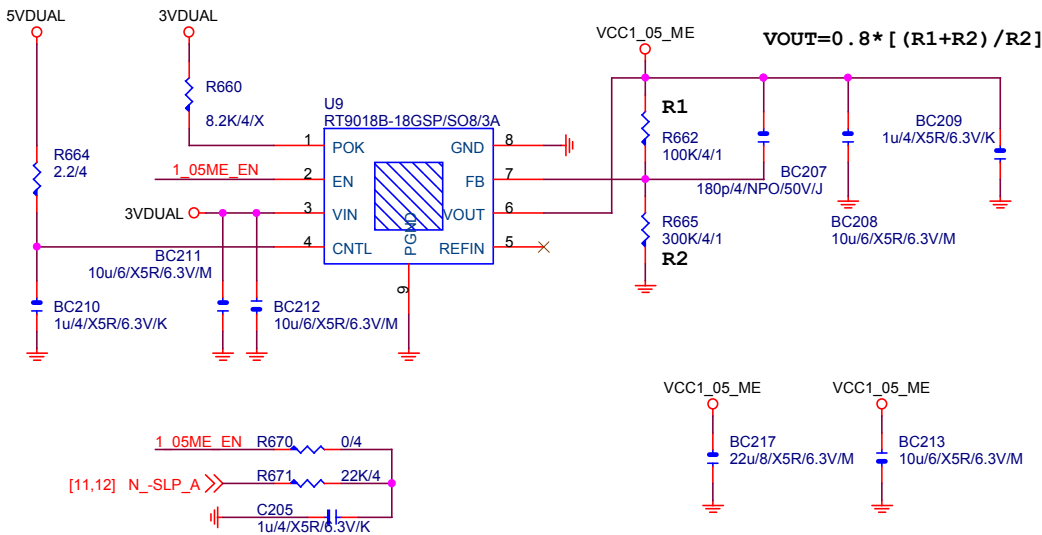

$$\begin{aligned} \text{Rocset} &= (\text{Iocp} * \text{Lgate}, \text{rdson}) / \text{Iocset} \\ \text{Rocset} &= (45\text{A} * 6.7\text{mOhm}) / 10\text{uA} = 30\text{K} \\ \text{Iocset} &= 10\text{uA} \end{aligned}$$

<i>Gigabyte Technology</i>			
Title			
DDR POWER			
Size	Document Number	GA-B85M-DS3H	Rev
Custom			1.1
Date:	Wednesday, August 28, 2013	Sheet	28 of 30

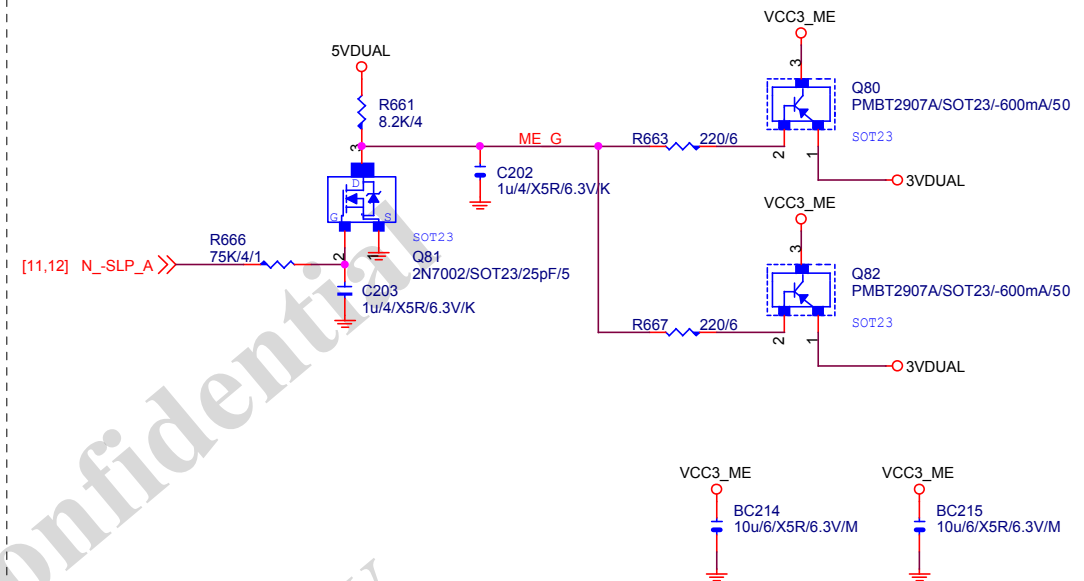
VCC1_05_ME

【技術通報R&D技術通報156】

(RICHTER), (NUVOTON), (EMC) 做共用
PIN7分壓阻值須做修改為100K以上電阻值



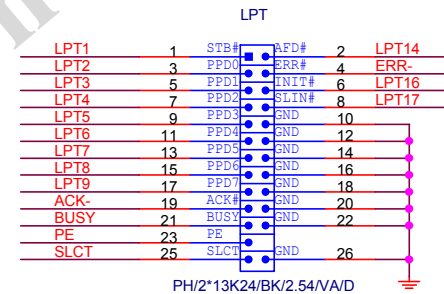
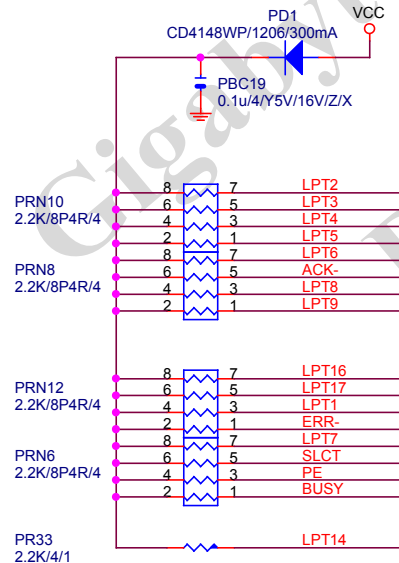
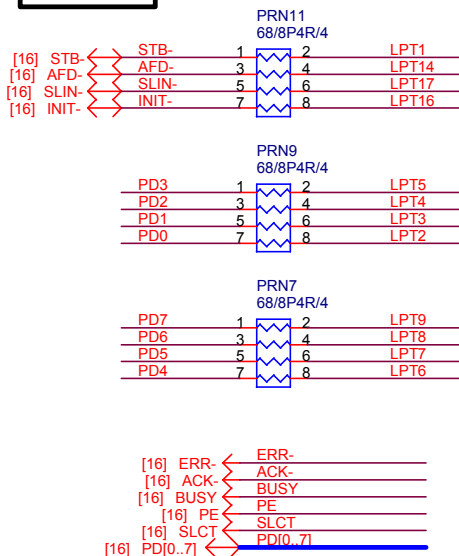
VCC3_ME



【技術通報R&D技術通報151】

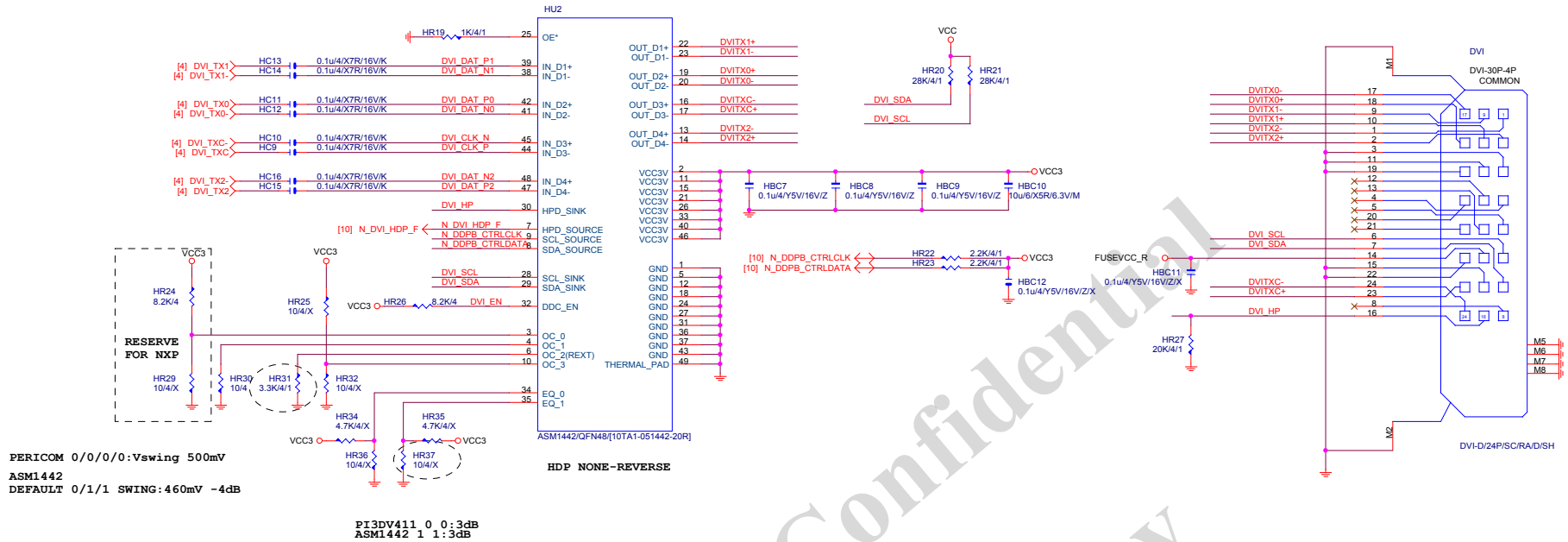
33ohm Change to 68ohm

LPT PORT

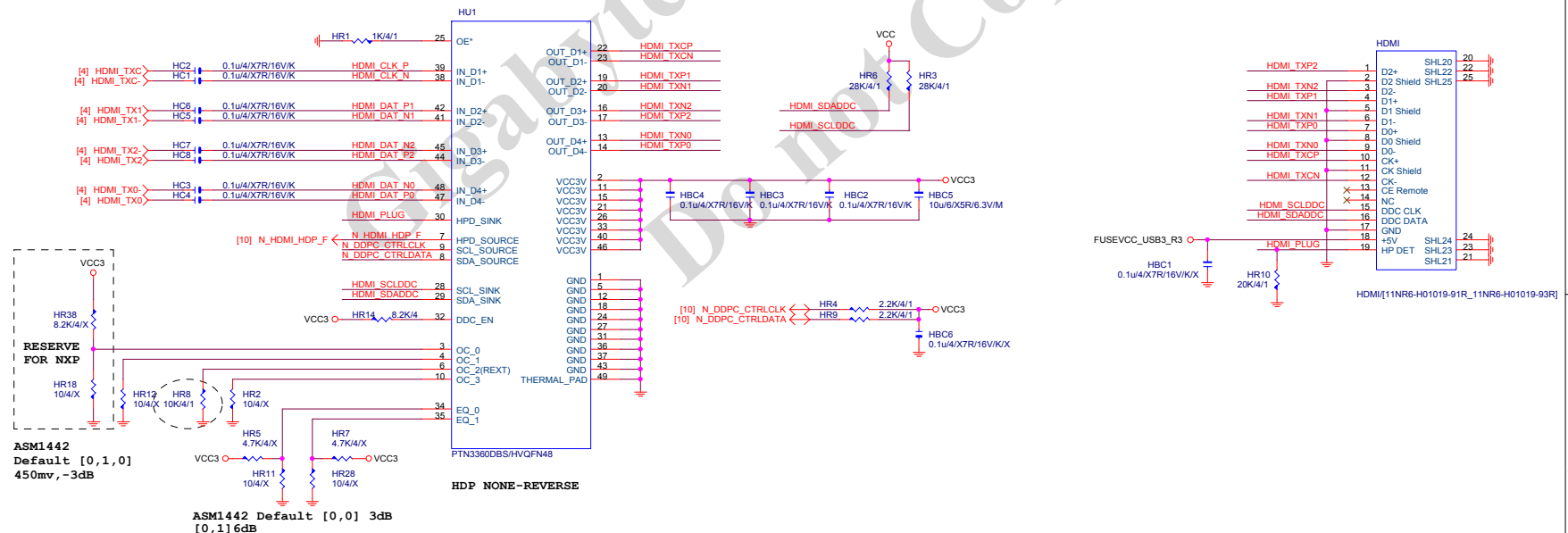


Gigabyte Technology			
Title			
LPT			
Size Custom	Document Number	GA-B85M-DS3H	
		Rev	1.1
Date:	Wednesday, August 28, 2013	Sheet	29 of 30

DVI LEVEL SHIFT



HDMI LEVEL SHIFT



Gigabyte Technology

File			DVI
Size	Document Number	GA-B85M-DS3H	
Custom		Rev	1.1
Date:	Wednesday, August 28, 2013	Sheet	30 of 30